



SURFACE MOUNT MODEL

VOLTAGE CONTROLLED OSCILLATOR

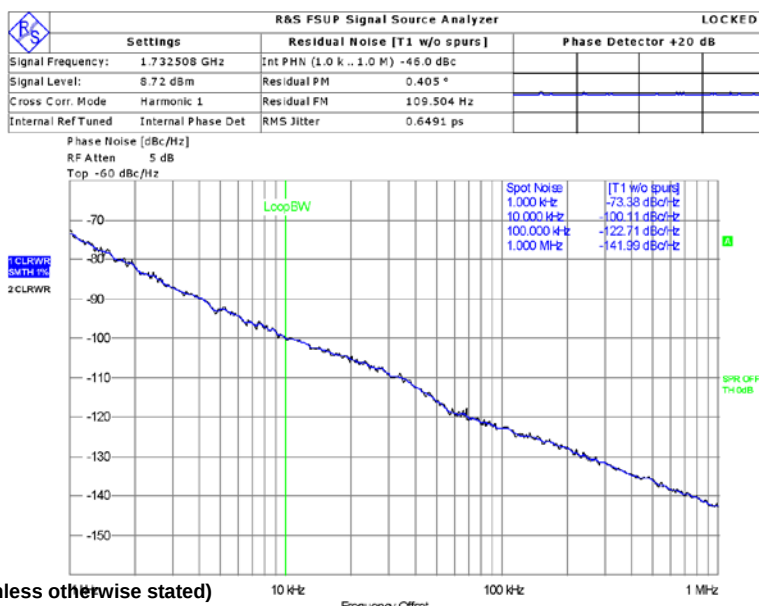
Frequency 1320-2120MHz

VL2120M



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning



Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5.0\text{V}$ (unless otherwise stated)

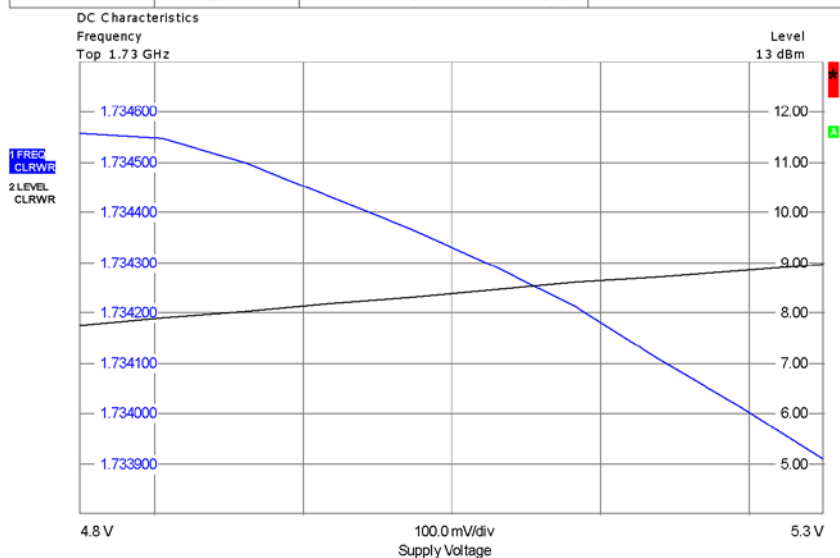
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs		1320		2120	MHz
Tuning Voltage (V)		1.0		15.0	V
RF Output Power	1320-2120MHz	6.0		10.0	dBm
Supply Voltage (Vcc)		4.75	5	5.25	V
Supply Current (Icc)			25	28	mA
Phase Noise :					
	@10kHz Offset:		-99	-95	dBc/Hz
	@100kHz Offset:		-121	-117	dBc/Hz
Average Tuning Sensitivity	1320-2120MHz		77		MHz/V
Harmonic Outputs			-15		dBc
OperatingTemperature Range		-40		+85	$^\circ\text{C}$
Tune Input Capacitance			18		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X4.3mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-40℃	+25℃	+85℃	-40℃	+25℃	+85℃	-40℃	+25℃	+85℃
1.0	120.0	1099.3	1074.2	1055.5	8.3	8.0	7.6	-	-	-
2.0	106.1	1211.2	1186.3	1170.7	8.3	8.2	7.6	-	-	-
3.0	90.1	1307.6	1285.6	1271.8	8.6	8.2	7.6	-	-	-
4.0	90.5	1394.8	1376.1	1363.2	8.7	8.1	7.5	-	-	-
5.0	85.5	1486.1	1465.2	1451.2	7.5	7.5	7.2	-	-	-
6.0	82.6	1568.0	1547.7	1524.9	7.8	7.8	7.3	-	-	-
7.0	73.2	1644.1	1626.1	1614.3	8.3	8.0	7.3	-	-	-
8.0	74.9	1720.4	1702.4	1690.3	8.3	7.8	7.1	-	-	-
9.0	72.3	1793.5	1775.2	1762.5	8.0	7.7	7.1	-	-	-
10.0	67.3	1863.4	1845.7	1833.5	8.0	7.8	7.1	-	-	-
11.0	71.8	1933.3	1916.8	1904.2	8.2	7.8	6.9	-	-	-
12.0	62.3	2004.7	1983.1	1966.5	7.6	7.0	6.7	-	-	-
13.0	62.8	2061.1	2042.1	2030.4	7.2	7.7	7.3	-	-	-
14.0	57.5	2123.3	2107.9	2094.1	8.2	7.4	6.2	-	-	-
15.0	49.4	2180.9	2151.9	2137.6	6.6	7.2	7.2	-	-	-

R&S FSUP Signal Source Analyzer					
Settings			Results		
Supply	Voltage		Frequency		Level
Vmin	4.75	V	1.735	GHz	7.74 dBm
Vcurrent	5.00	V	1.734	GHz	8.40 dBm
Vmax	5.25	V	1.734	GHz	8.97 dBm



Measurement Aborted

Date: 7.AUG.2009 16:47:06

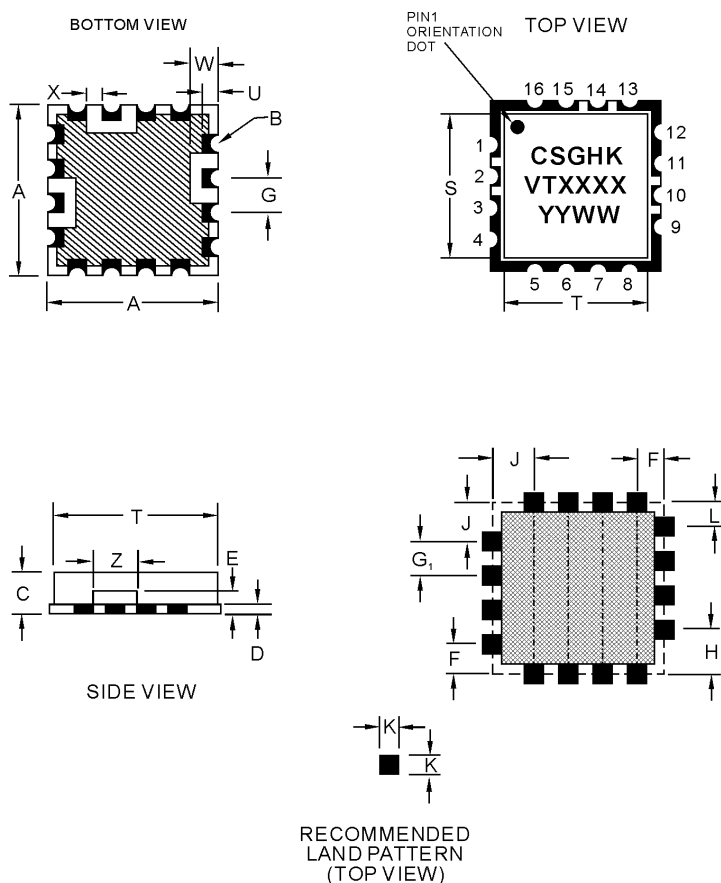
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	4.3	0.169
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			